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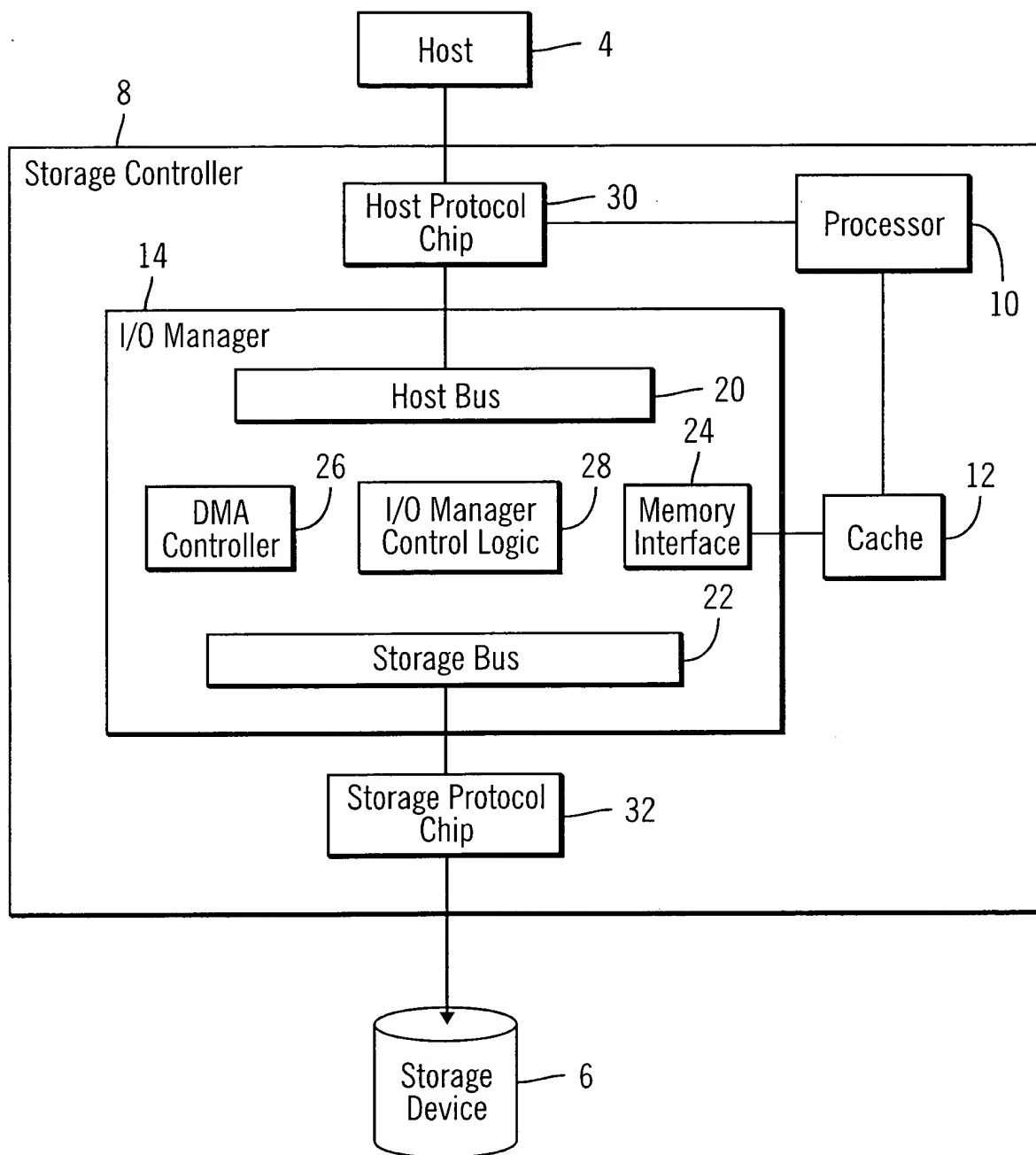
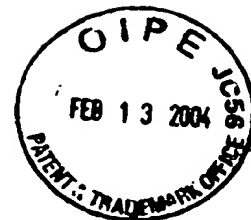


FIG. 1



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50

Hardware Control Block	
Byte	Field
<u>0-5</u> 0 - 1 2 - 5	<u>Target PA</u> Disk ID LBA
6-7	Reserved for processor.
8-23	Reserved for I/O Manager.
<u>24-25</u> Bits 0 - 9 Bit 10 Bit 11 Bit 12 Bit 13 Bits 14 - 15	<u>Controls</u> Reserved Recalculate LRC after Target PA is added and add LRC. Add target PA; use the target PA. The LBA is used for the first transferred sector. Increment the LBA for subsequent transferred sector. Check LRC. Check PA. Use the source PA. LBA must match first transferred sector. Increment the LBA for subsequent transferred sector. Address Conversion: 00 - 512 cache to 512 host. 01 - 512 host to 520 cache. 10 - 520 cache to 512 disk. 11 - 520 cache/disk.
<u>26-31</u> 26-27 28-31	<u>Source PA</u> Disk ID LBA

FIG. 2



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60

ADDRESS FORMAT	
Bit Offset	Field Description
63	Reserved
62	Hardware Control Block enabled (0 - do not use HCB; 1 - use HCB index).
61-64	Hardware Control Block Index.
43-33	Reserved
32-0	Memory Address

FIG. 3



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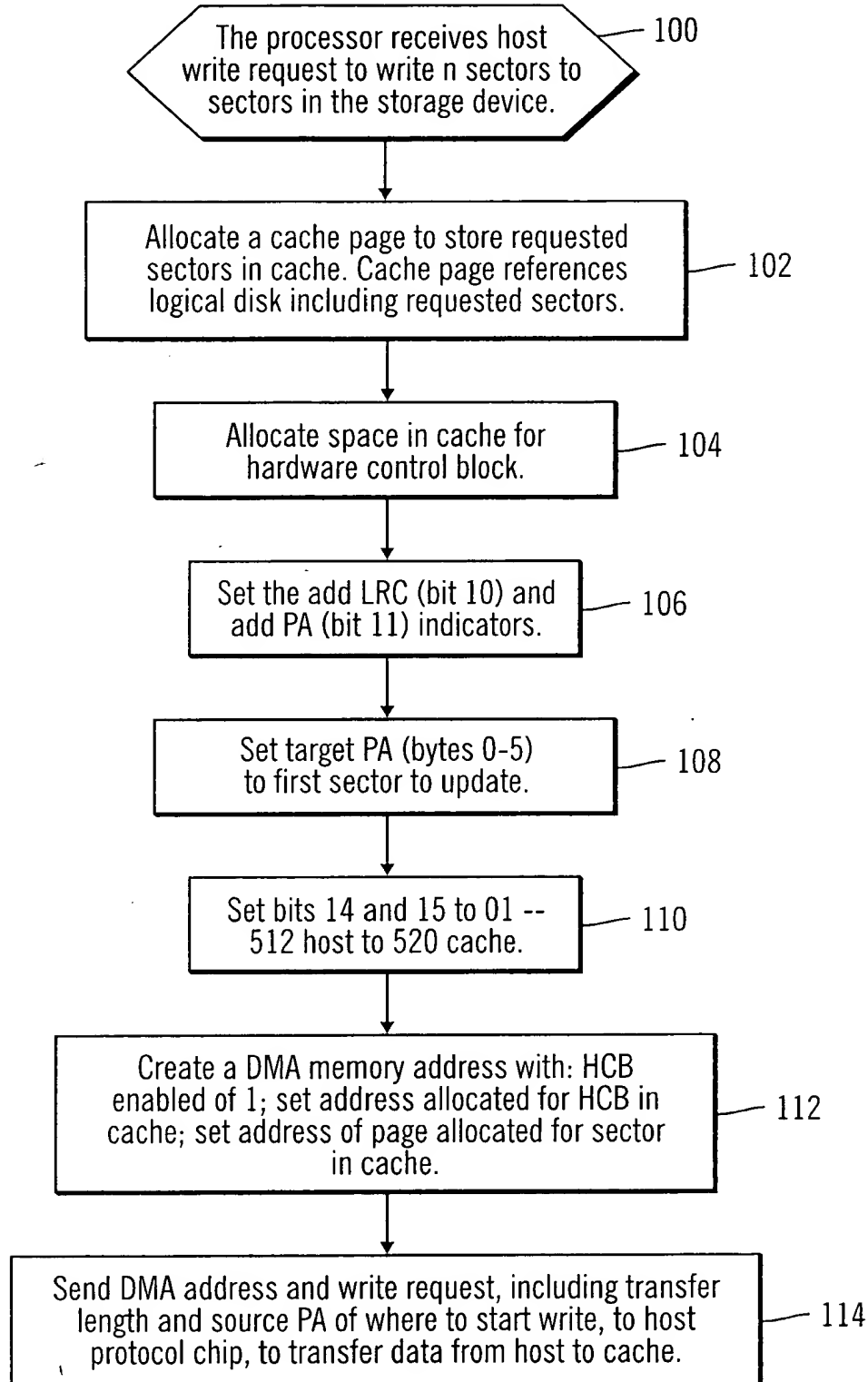
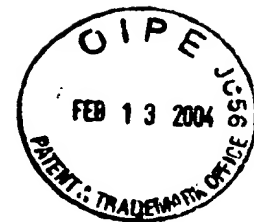


FIG. 4



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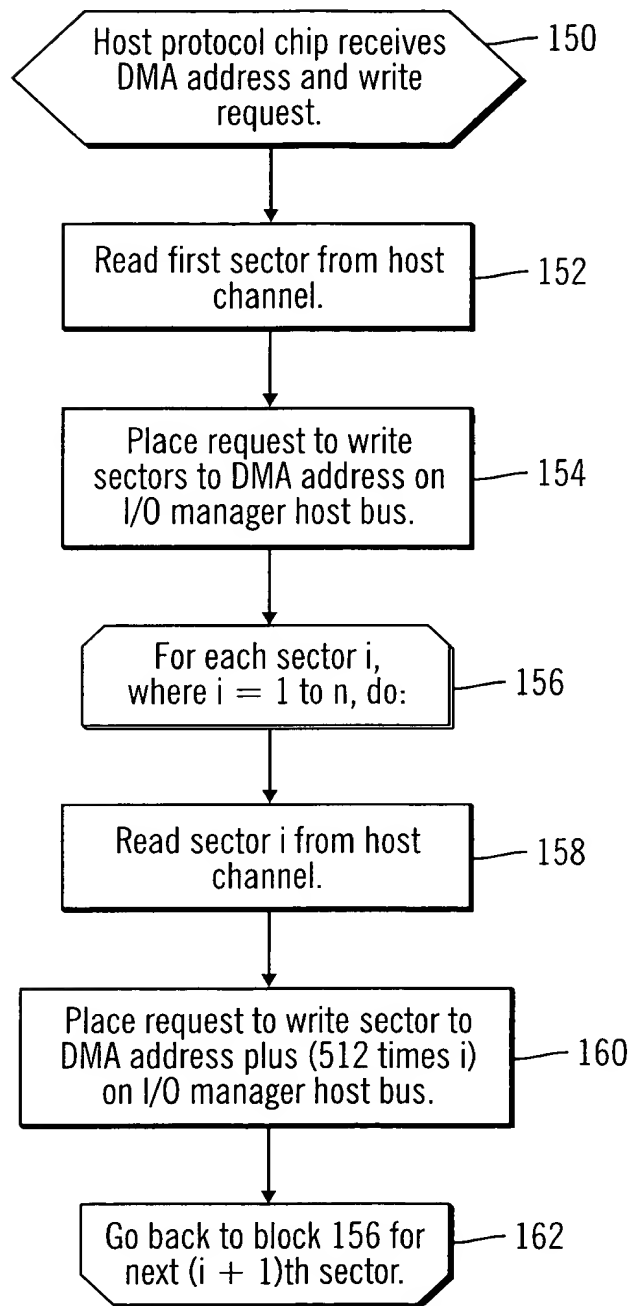


FIG. 5

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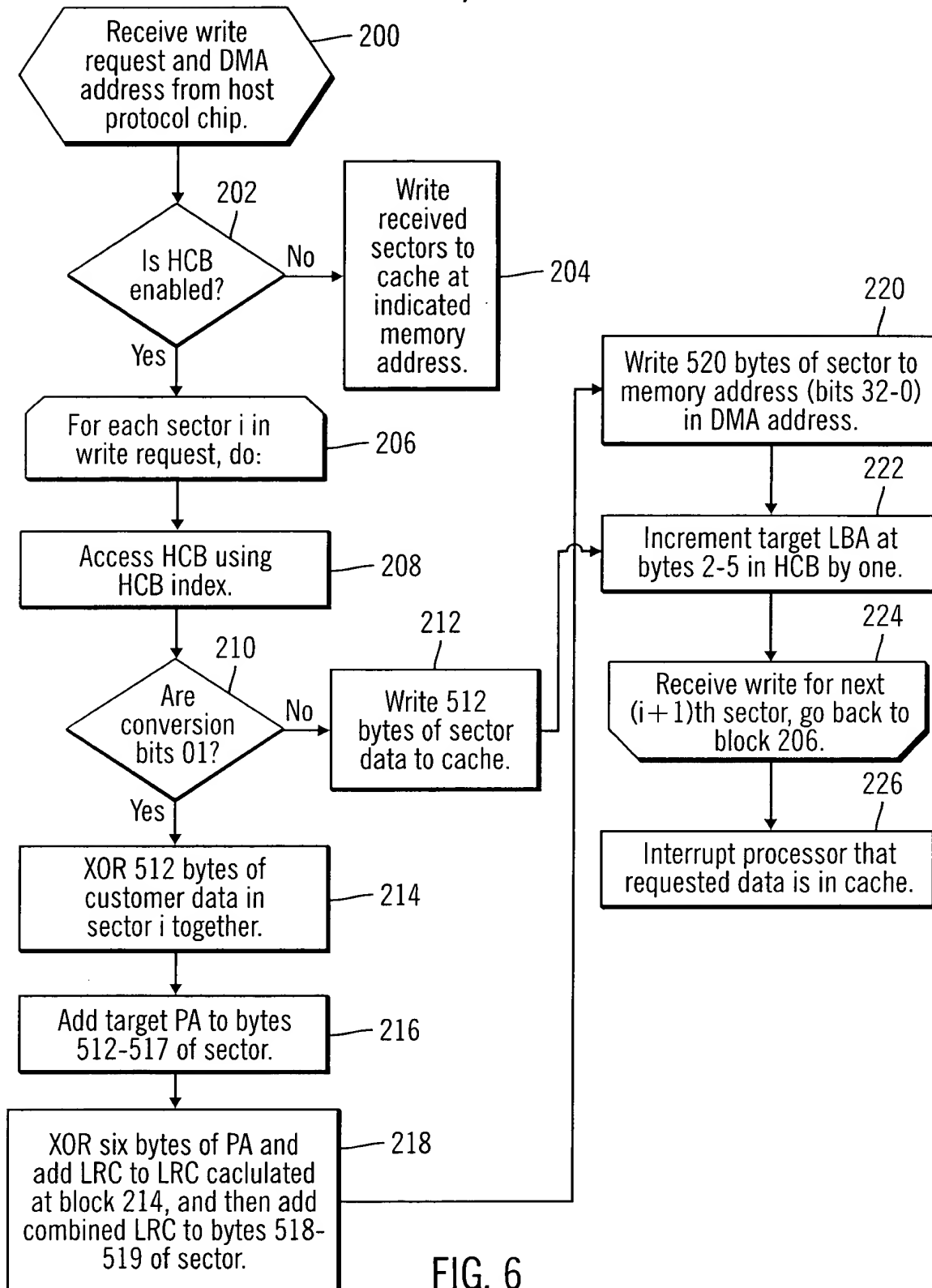


FIG. 6



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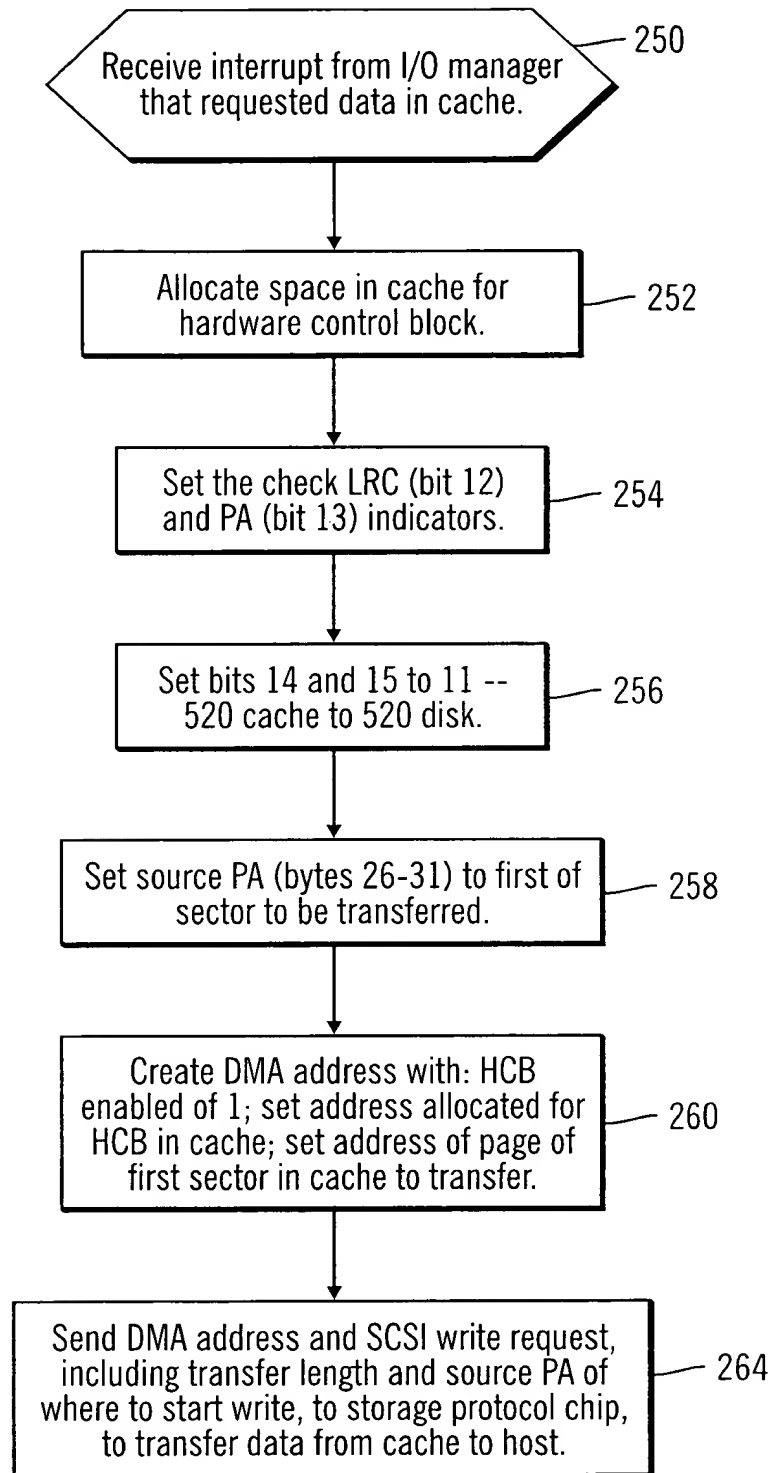


FIG. 7



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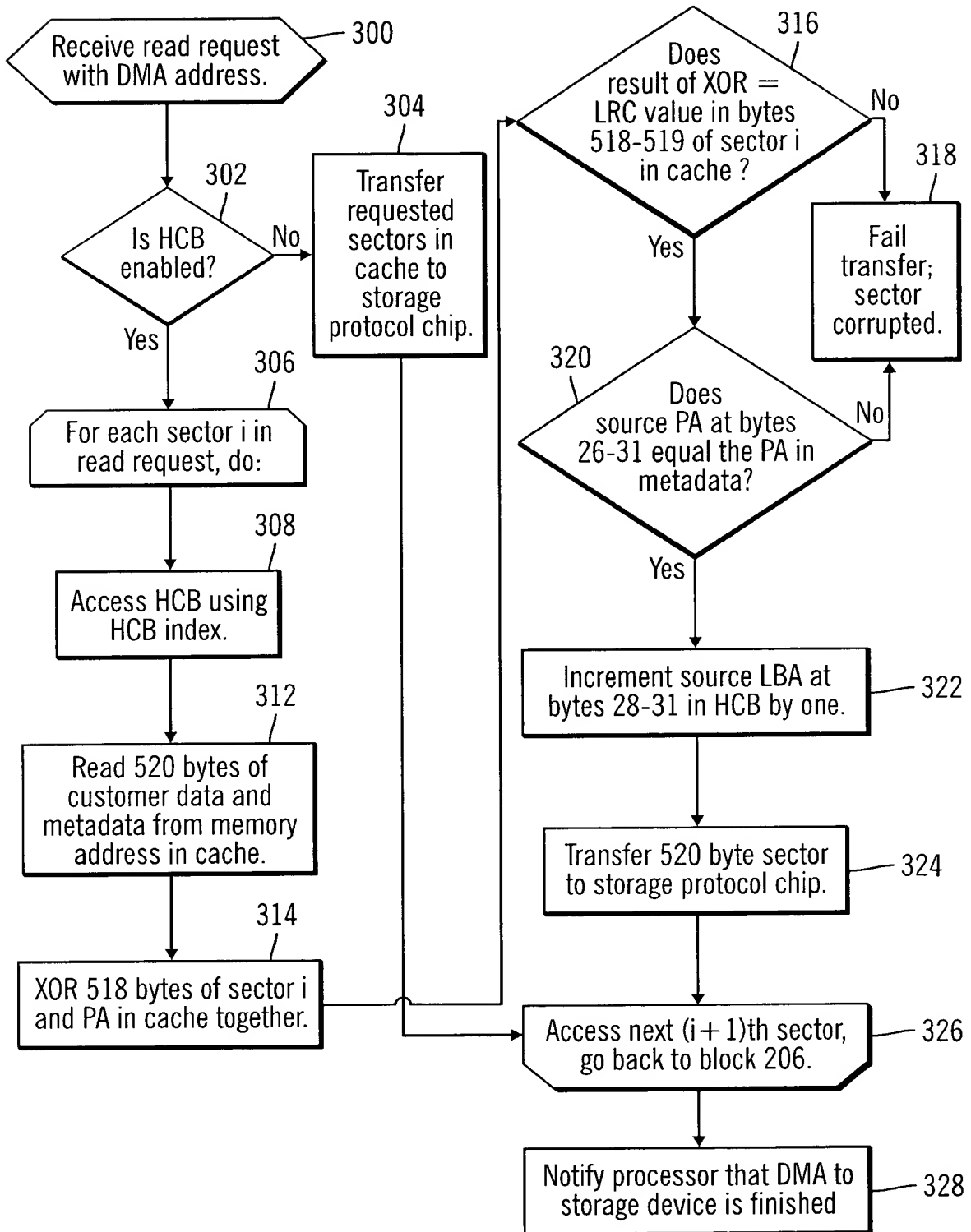


FIG. 8



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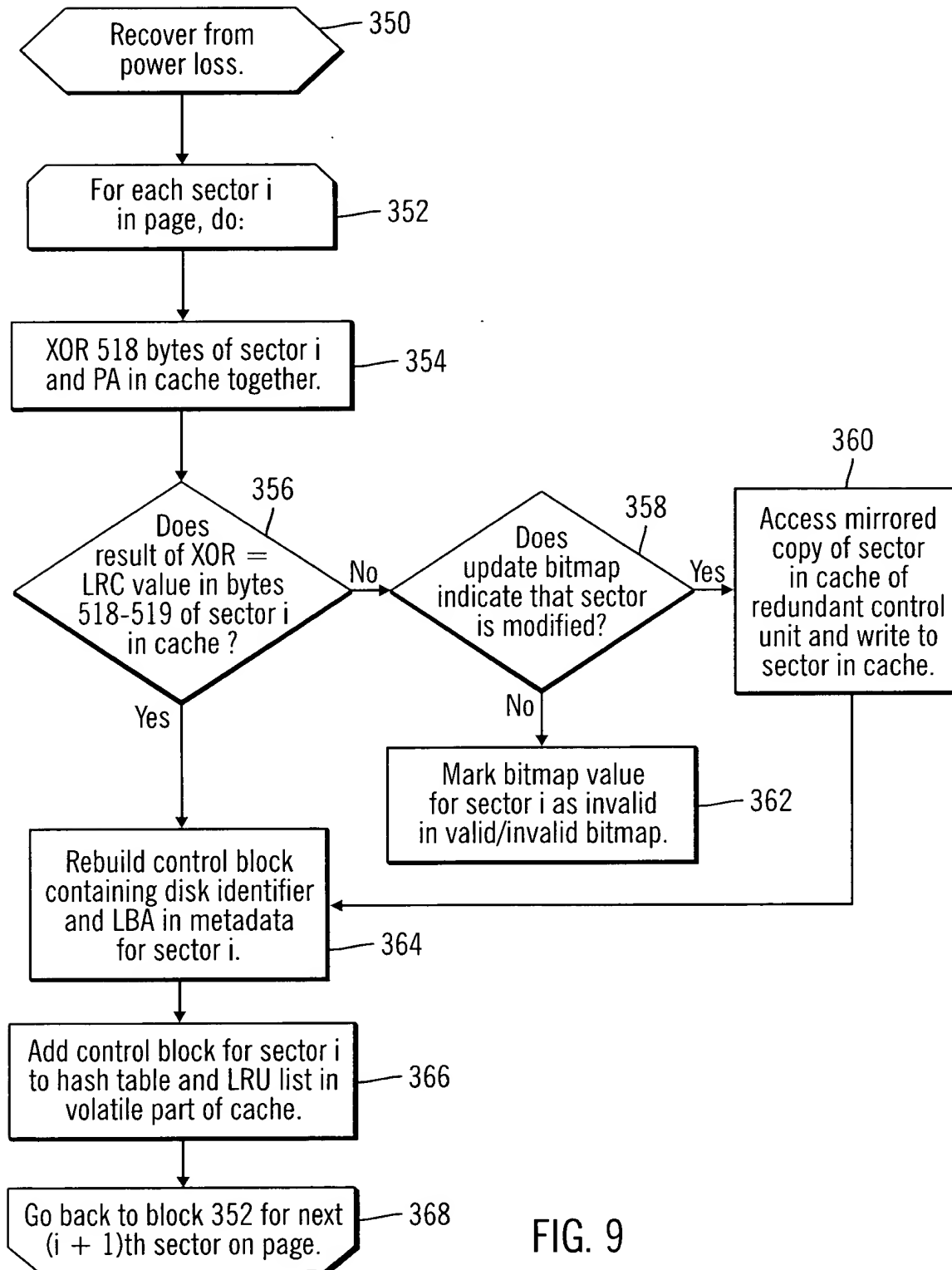


FIG. 9